

Session 4: FPGAs and Programming Models

Operating Systems for FPGAs – Why and How?

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Abstract

This talk introduces methods for developing, maintaining and running FPGA-accelerated systems from embedded to datacenters. We discuss FPGA hardware abstractions, programming models and APIs to applications and examine security and performance aspects related to FPGAs. We will also dive into the question if FPGA-enabled datacenters should support multi-tenancy?